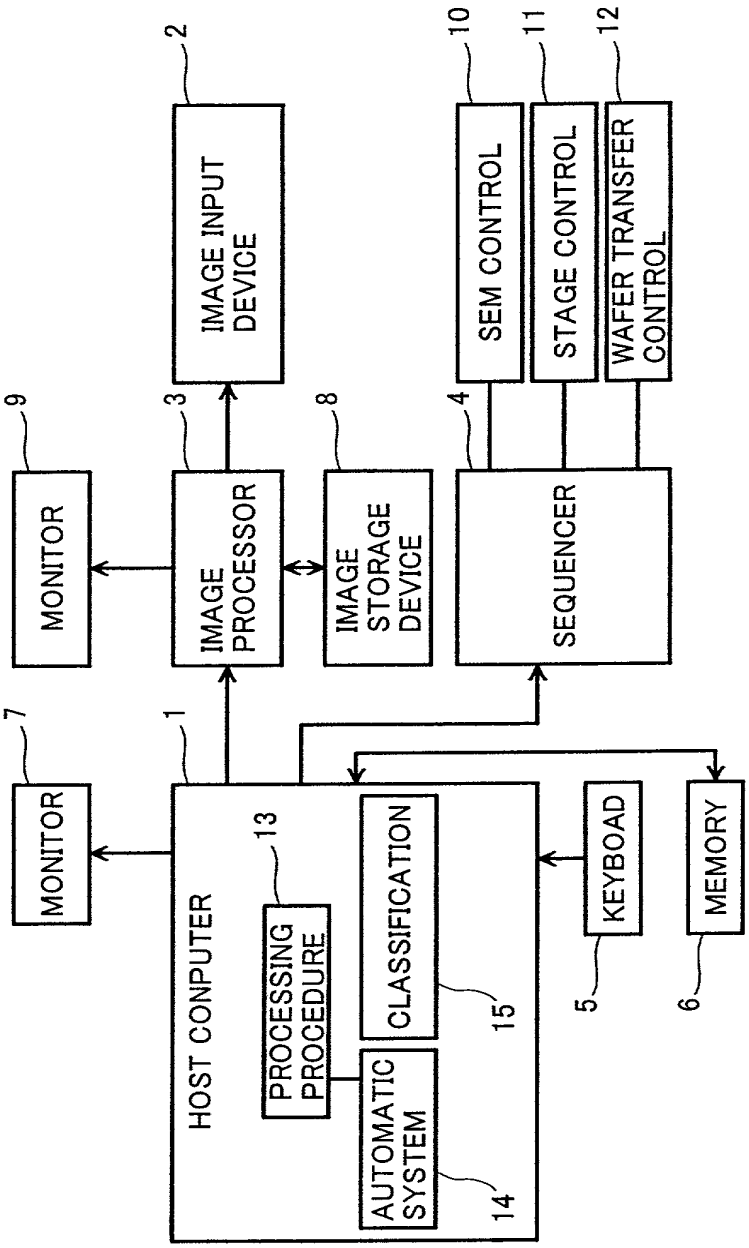
FIG. 1


HOST CONPUTER
MONITOR
IMAGE PROCESSOR
IMAGE INPUT DEVICE
IMAGE STORAGE DEVICE
SEQUENCER
SEM CONTROL
STAGE CONTROL
WAFER TRANSFER CONTROL
KEYBOAD
MEMORY
PROCESSING PROCEDURE
CLASSIFICATION
AUTOMATIC SYSTEM
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15

JP 2005-345161 A 2005.12.15

FIG. 2

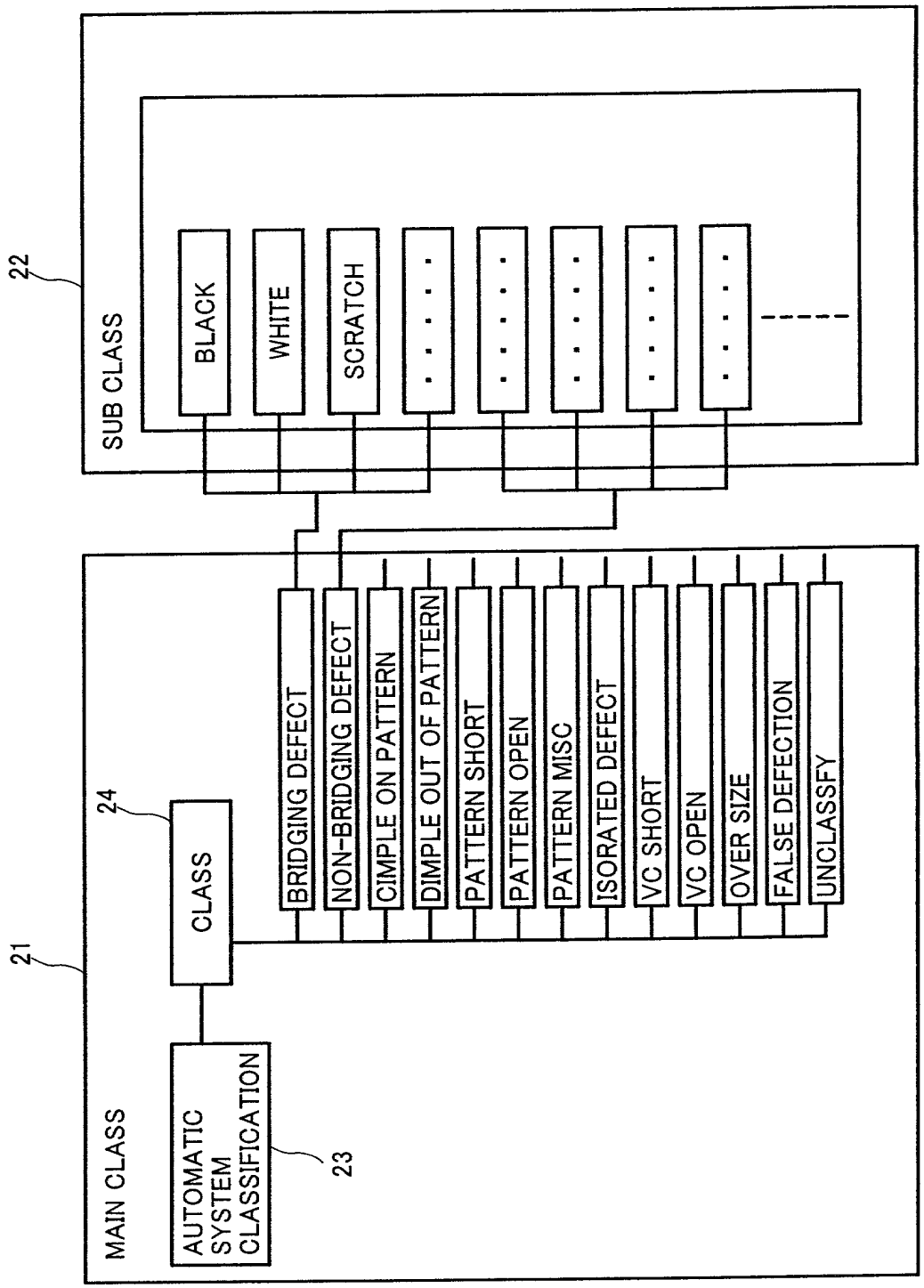


FIG. 3

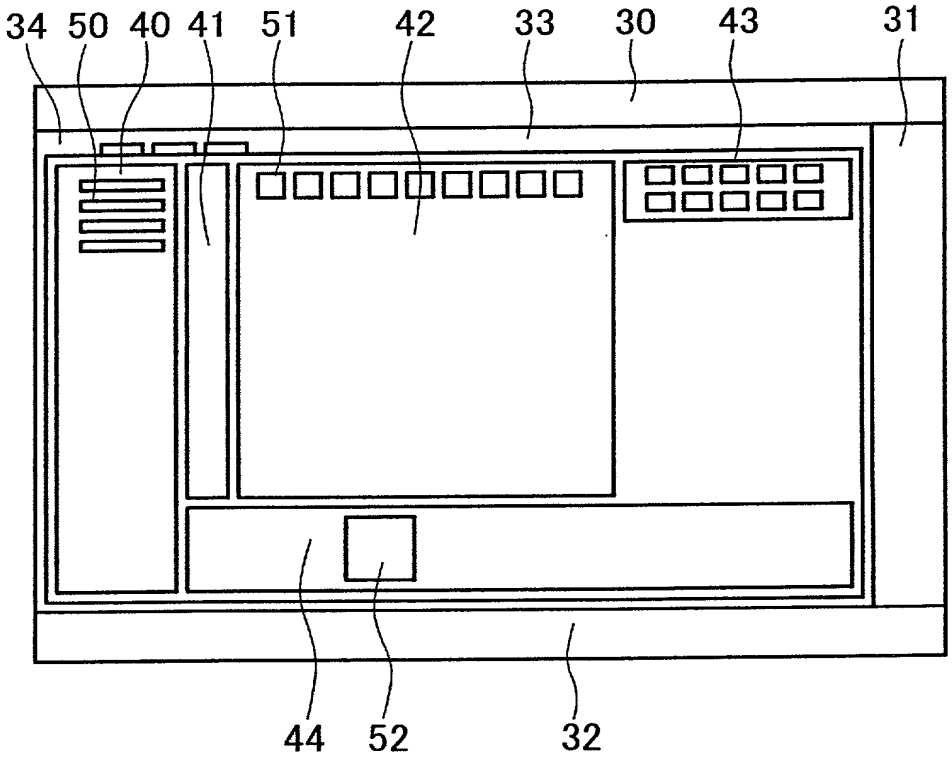


FIG. 4A FIG. 4B FIG. 4C

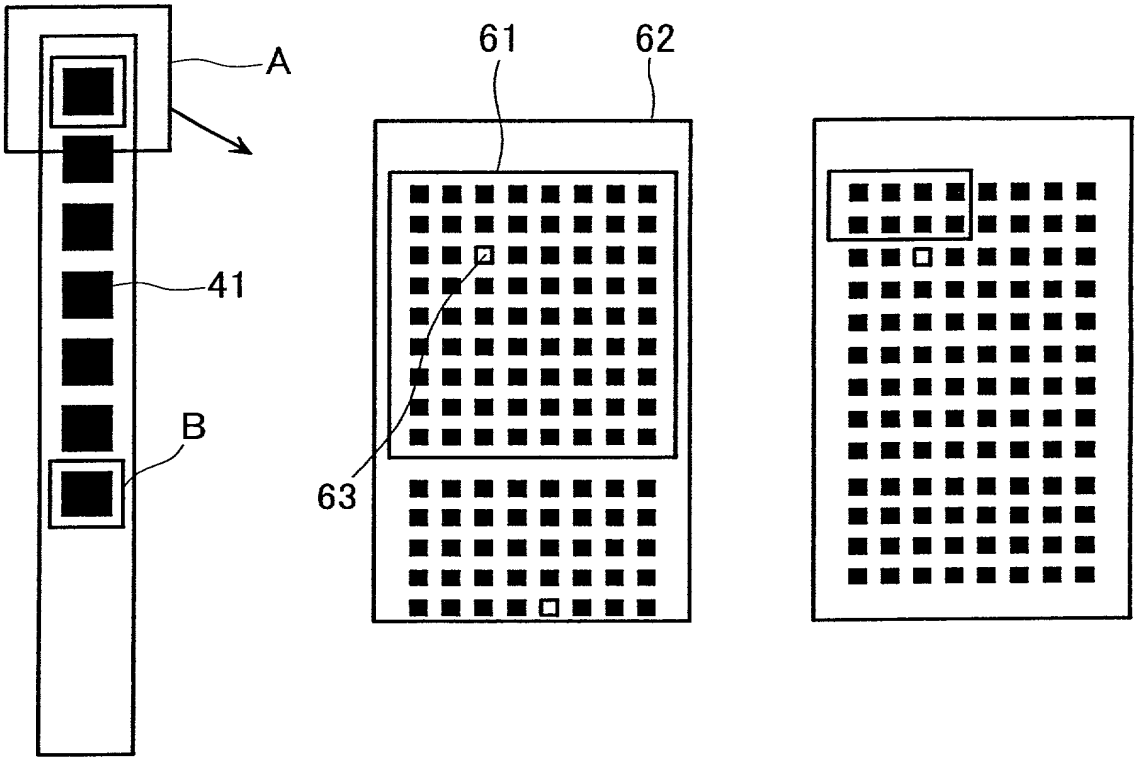


FIG. 5

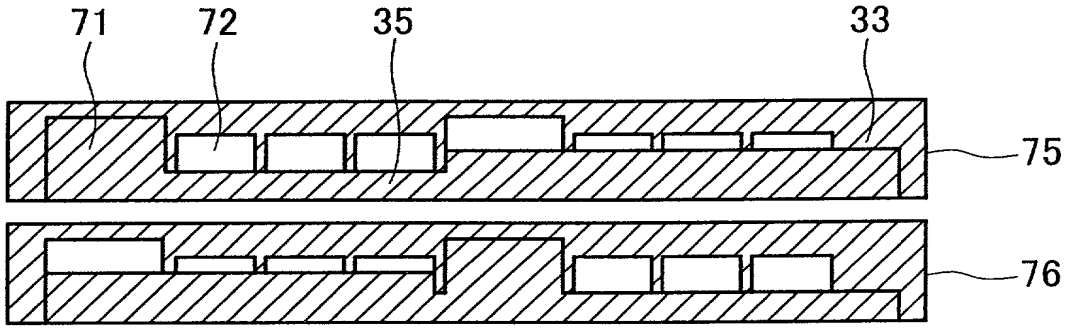


FIG. 6

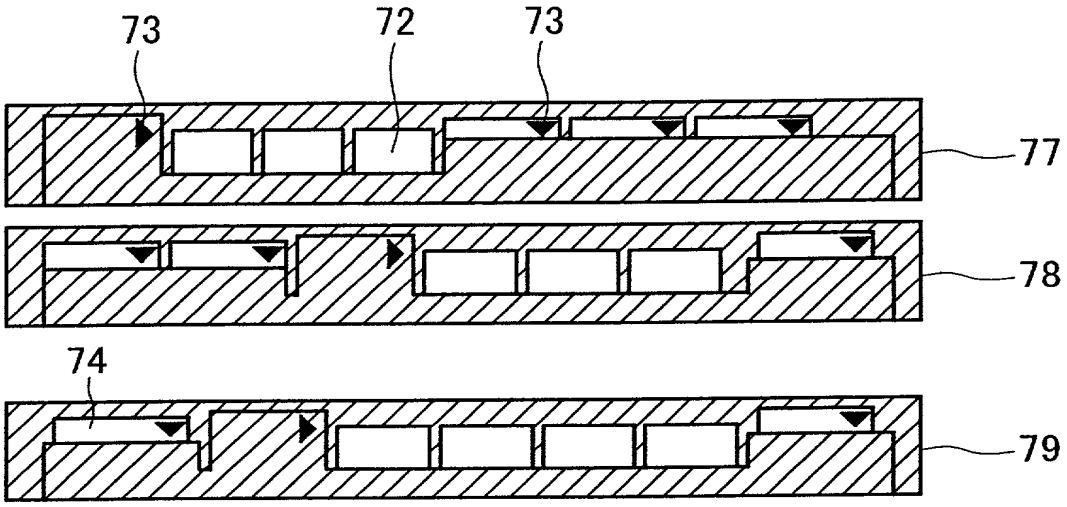
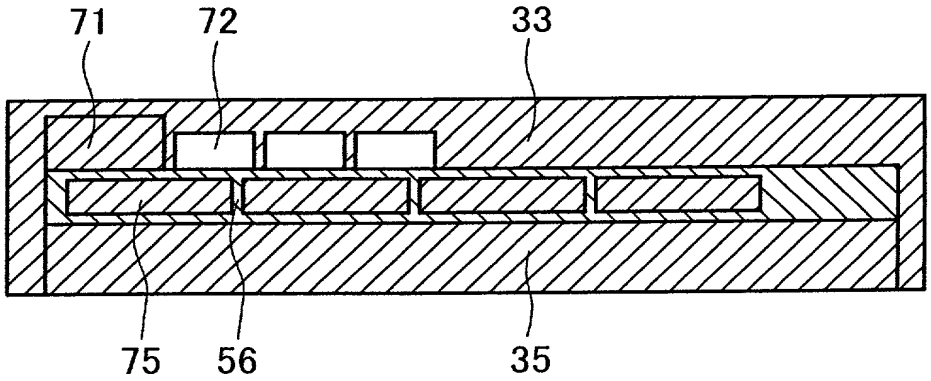


FIG. 7



TOSHIBA 94-95860

FIG. 8

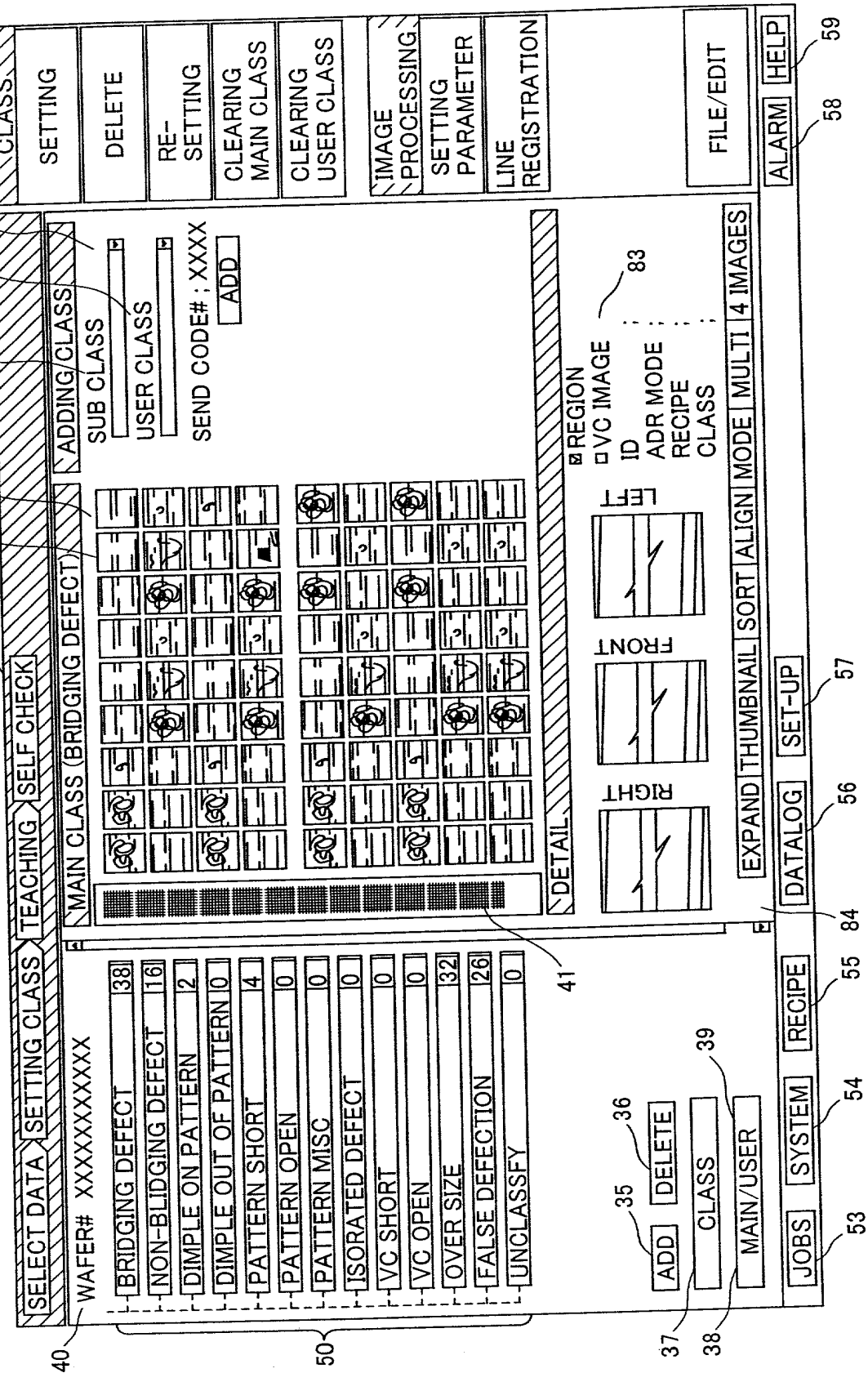


FIG. 9

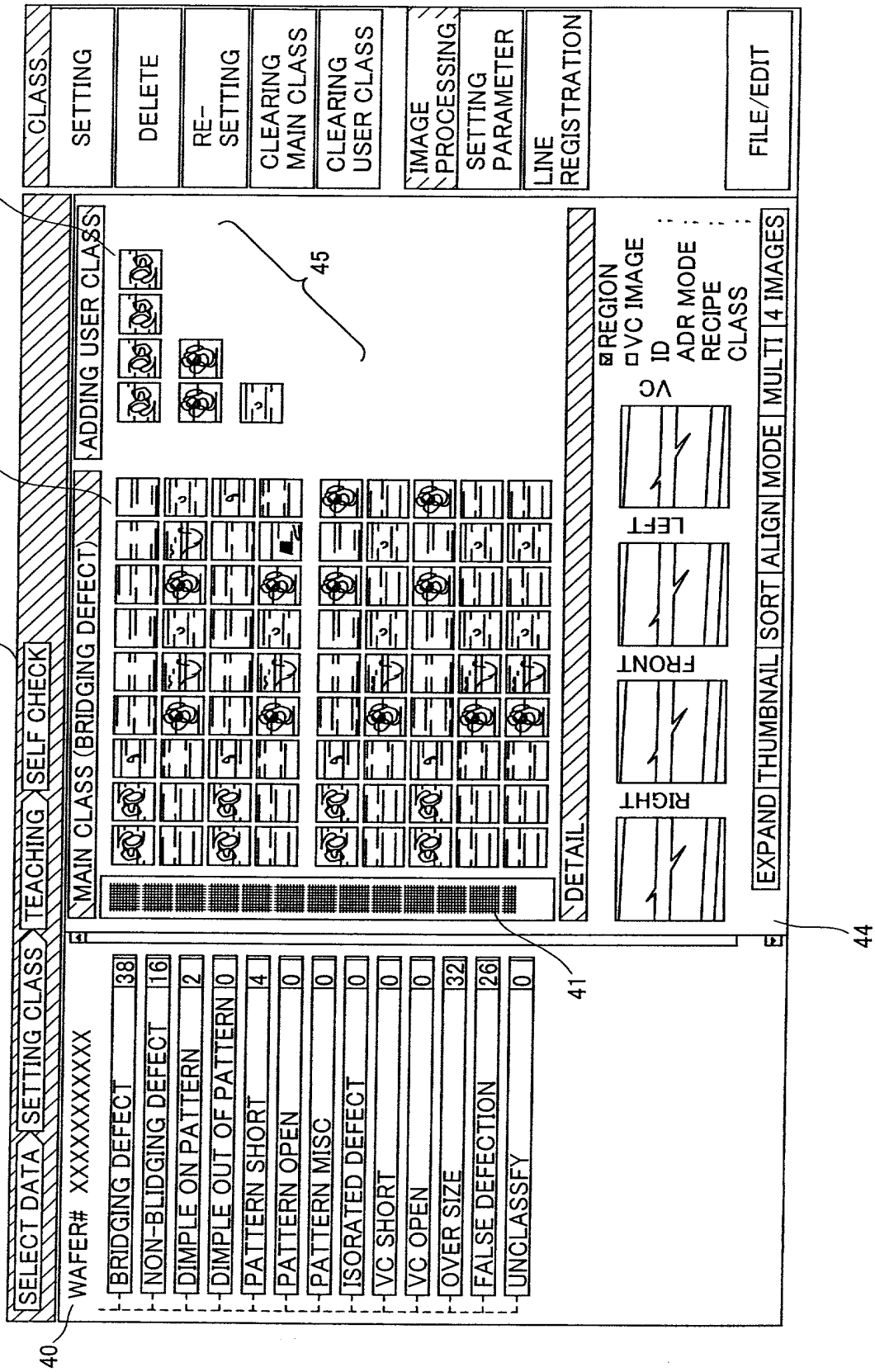
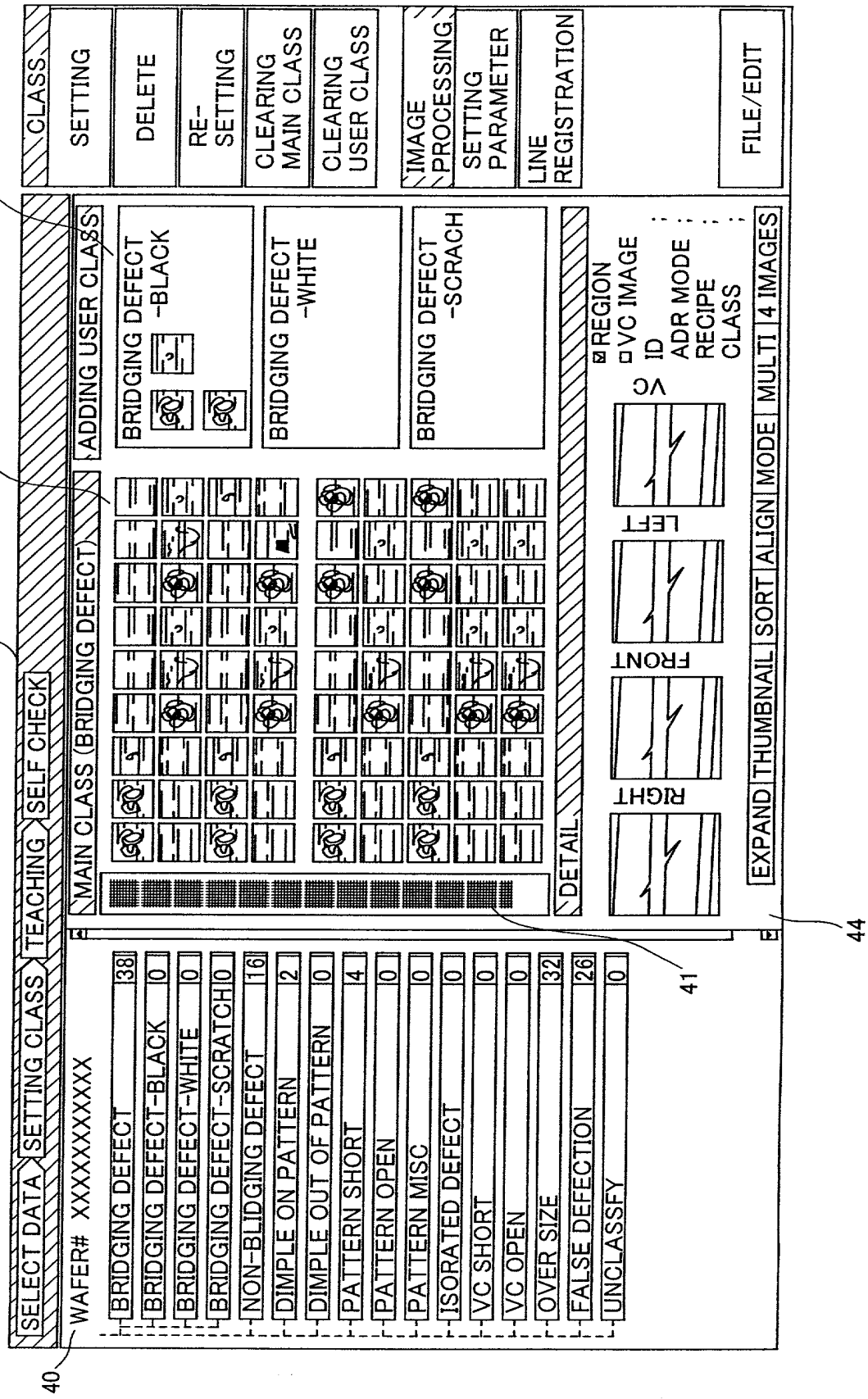

CLASS
SETTING
DELETE
RE-SETTING
CLEARING MAIN CLASS
CLEARING USER CLASS
IMAGE PROCESSING
SETTING PARAMETER
LINE REGISTRATION
FILE/EDIT
SELECT DATA
SETTING CLASS
TEACHING
SELF CHECK
ADDING USER CLASS
MAIN CLASS (BRIDGING DEFECT)
DETAIL
REGION
VC IMAGE
ID
ADR MODE
RECIPE
CLASS
VC
LEFT
FRONT
RIGHT
EXPAND
THUMBNAIL
SORT
ALIGN MODE
MULTI
4 IMAGES
WAFER# XXXXXXXXX
BRIDGING DEFECT 38
NON-BLIDGING DEFECT 16
DIMPLE ON PATTERN 2
DIMPLE OUT OF PATTERN 0
PATTERN SHORT 4
PATTERN OPEN 0
PATTERN MISC 0
ISORATED DEFECT 0
VC SHORT 0
VC OPEN 0
OVER SIZE 32
FALSE DEFECTION 26
UNCLASSFY 0
34
40
41
42
43
44
45

FIG. 10


CLASS
SETTING
DELETE
RE-SETTING
CLEARING MAIN CLASS
CLEARING USER CLASS
IMAGE PROCESSING
SETTING PARAMETER
LINE REGISTRATION
FILE/EDIT
SELECT DATA
SETTING CLASS
TEACHING
SELF CHECK
WAFER# XXXXXXXXX
ADDING USER CLASS
MAIN CLASS (BRIDGING DEFECT)
BRIDGING DEFECT -BLACK
BRIDGING DEFECT -WHITE
BRIDGING DEFECT -SCRACH
DETAIL
REGION
VC IMAGE
ID
ADR MODE
RECIPE
CLASS
VC
LEFT
FRONT
RIGHT
EXPAND
THUMBNAIL
SORT
ALIGN
MODE
MULTI
4 IMAGES
BRIDGING DEFECT 38
BRIDGING DEFECT-BLACK 0
BRIDGING DEFECT-WHITE 0
BRIDGING DEFECT-SCRATCH 0
NON-BLIDGING DEFECT 16
DIMPLE ON PATTERN 2
DIMPLE OUT OF PATTERN 0
PATTERN SHORT 4
PATTERN OPEN 0
PATTERN MISC 0
ISORATED DEFECT 0
VC SHORT 0
VC OPEN 0
OVER SIZE 32
FALSE DEFECTION 26
UNCLASSFY 0
40
41
42
43
44

JP 2003-346150 A 2003.12.05

FIG. 11

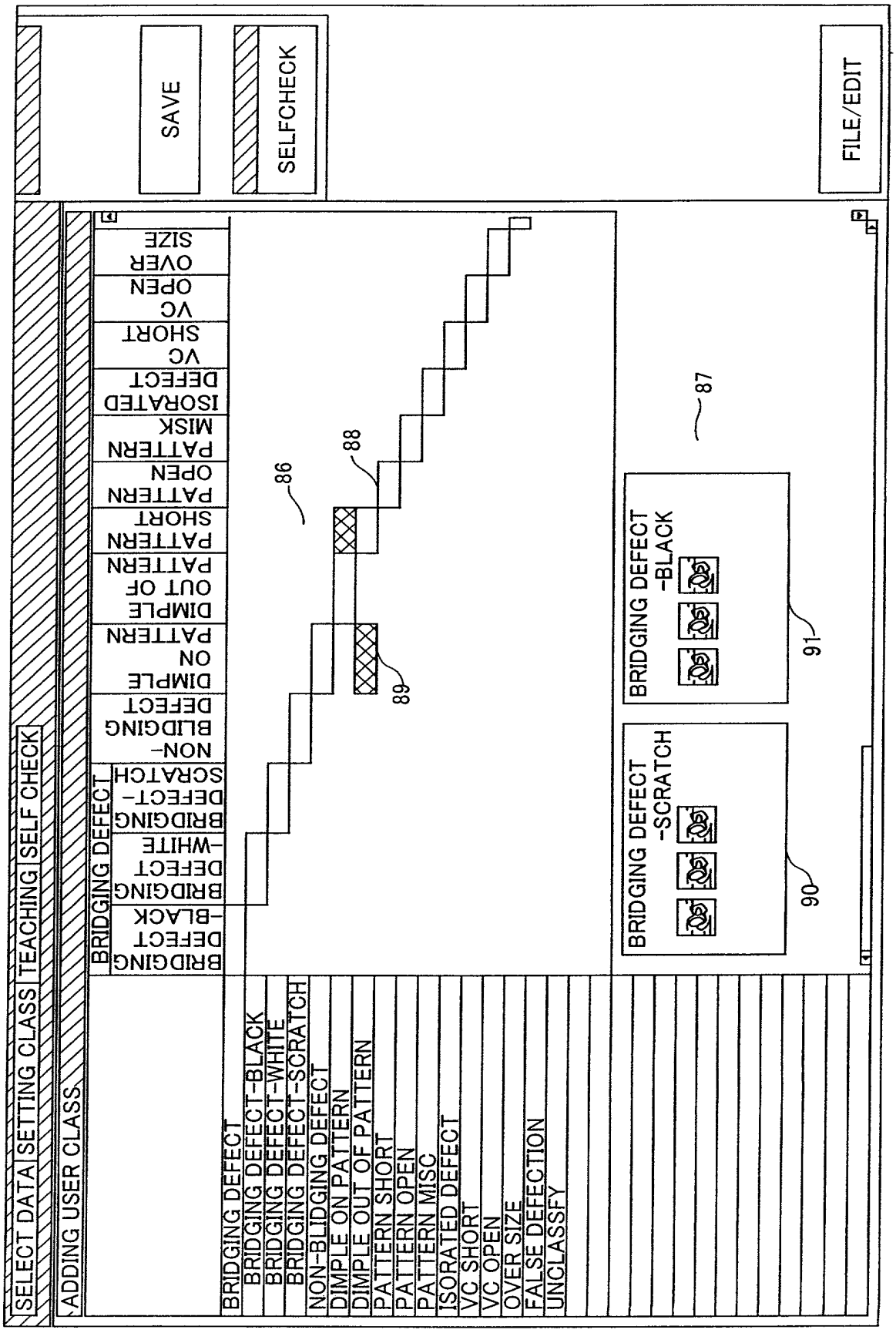
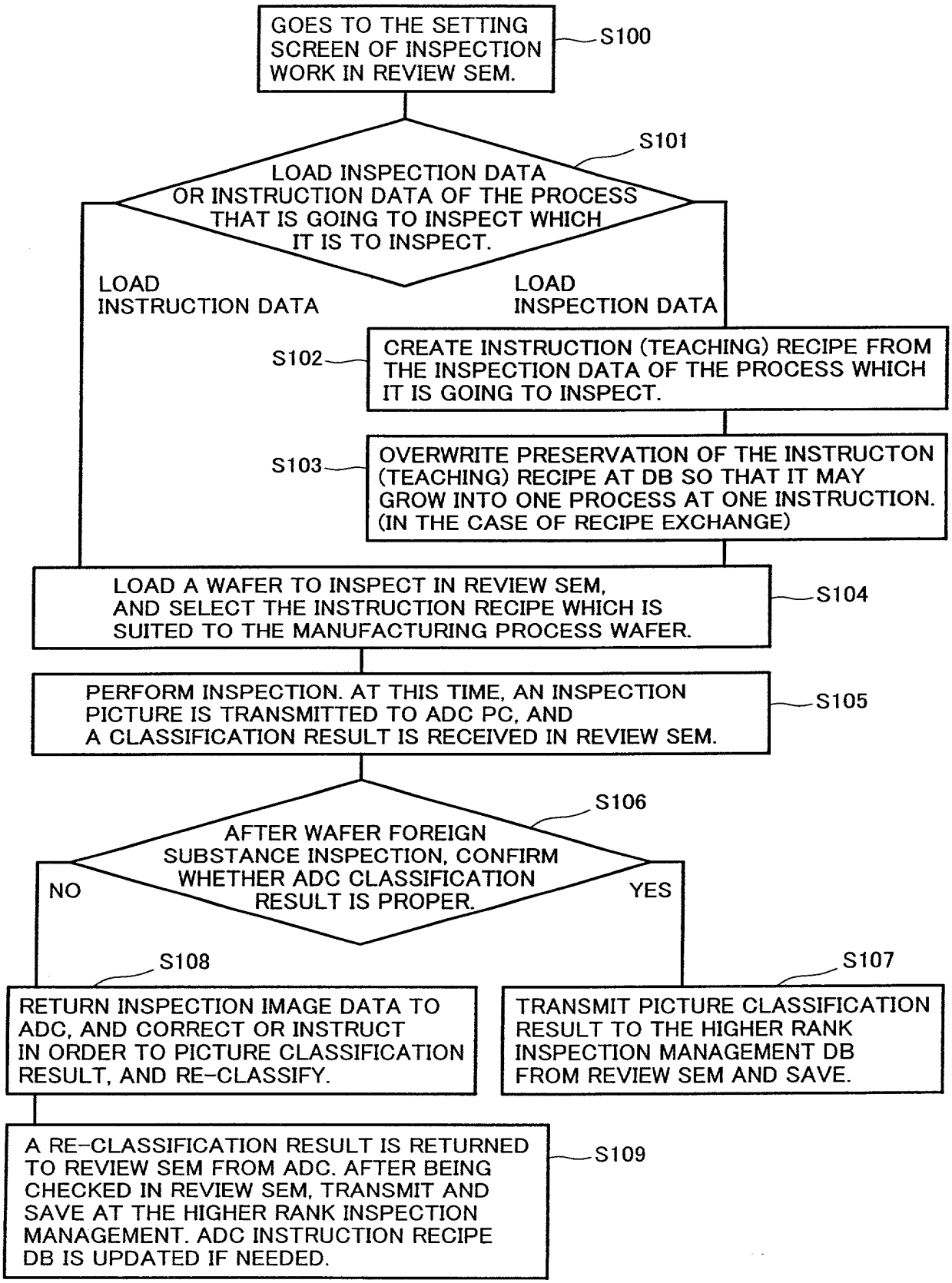


FIG. 12



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